

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,219,201 B2
APPLICATION NO. : 10/748886
DATED : May 15, 2007
INVENTOR(S) : Naohisa Kasako et al.

Page 1 of 1

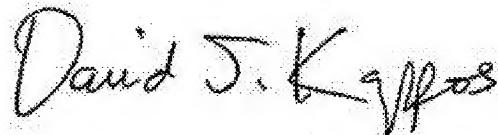
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the specification,

Column 4, lines 16-29, should read:

FIG. 2 shows a structure of a disk array device, which is described as an example of the first storage device 10 and the second storage device 20. Instead to of the disk array device, the first and second storage devices 10 and 20 may be any appropriate devices, such as, for example, semiconductor storage devices. For example, the disk array device 10 is equipped with various components including a channel control section 201, a remote communications interface 202, disk control sections 203, a shared memory 204, a cache memory 205, a switching control section 206 that is composed of cross bus switches that communicatively connect the components described above, a management terminal 207, and memory devices 208. The first and second storage devices 10 and 20 may have the same structure.

Signed and Sealed this
Fourteenth Day of June, 2011



David J. Kappos
Director of the United States Patent and Trademark Office